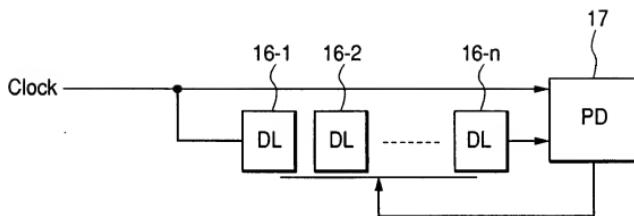




3 / 20

FIG. 3
PRIOR ART



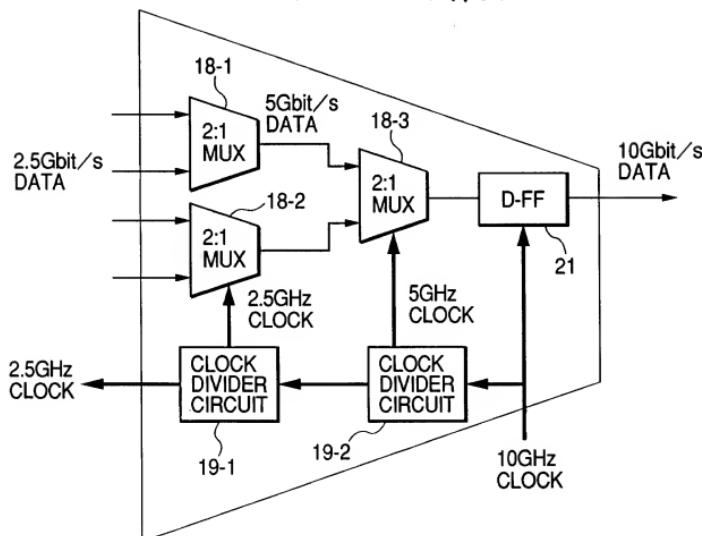
DL: DELAY LINE

PD: PHASE DETECTOR



4 / 20

FIG. 4
PRIOR ART





5 / 20

FIG. 5
PRIOR ART

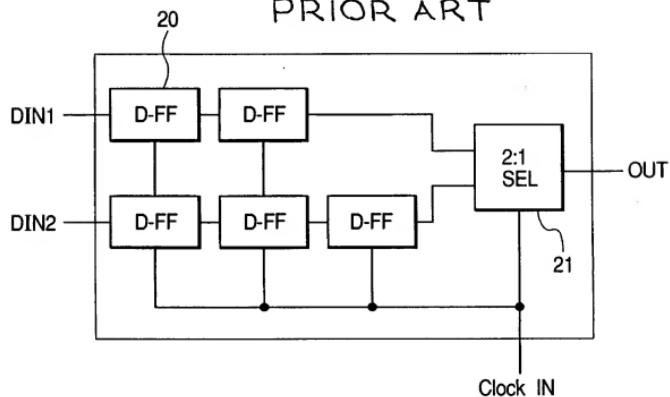
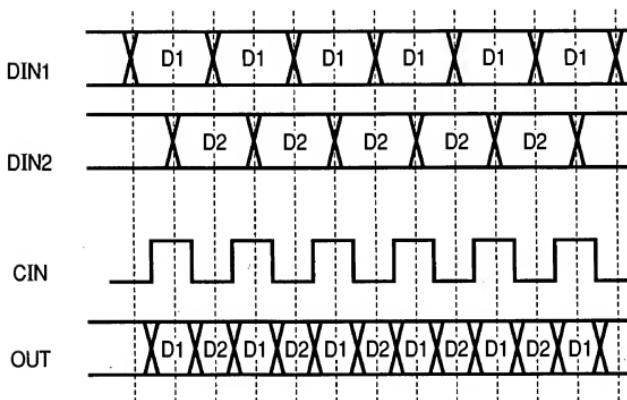


FIG. 6
PRIOR ART



15 / 20

FIG. 15
PRIOR ART

